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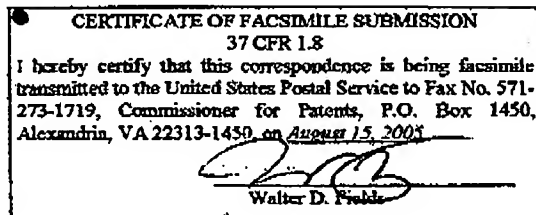
AUG 15 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Andrew E. Horch	§	Group Art Unit: 2814
Serial No.: 10/639,058	§	Examiner: Vikki H. Trinh
Filed: August 11, 2003	§	Confirmation No. 6760
Title: Thyristor Semiconductor Memory	§	Atty. Docket: 2000.004.00/US
Device and Method of Manufacture	§	Date: August 15, 2005

**SUPPLEMENTAL AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450



Dear Sir:

Applicant submits this amendment responsive to the Examiner's informality inquiry of earlier today and supplemental to applicant's previous amendment of June 20, 2005, which was submitted responsive to the Office Action dated March 30, 2005.

Amendment to the Specification begins on page 2.

Remarks/Arguments begin on page 3.